OIPE JC141 JAN 20 2004 PATENT & TRADEMARK OFFICE

Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No.	Serial No.
		INTEL/17582	10/675,486
		Applicant	
		Rothman et al.	
		Filing Date	Group Art Unit
		9/30/2003	2127

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes	Translation No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		"Extensible Firmware Interface Specification." Version 1.10. Intel Corporation. December 1, 2002. Pages: all.
		Doran, M. "From Research and Development at Intel: Beyond the BIOS." Intel Developer Forum, Spring 2003. February 18, 2003. Pages 1-36.
		"Award Bios 4.51PG Postcodes." Shuttle Connecting Technologies, Support Website. [Retrieved on 2003-08/21]. Retrieved from internet: <URL: http://de.shuttle.com/postcode.htm>.
		"BIOS: ALL ABOUT IT." [Retrieved on 2003-08/21]. Retrieved from internet: <URL: http://www.programmersheaven.com/wiki/printer.aspx?w=1&p=BIOS>.

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

OIPE JC141
JAN 20 2004
PATENT & TRADEMARK OFFICE

PATENT
INTEL/17582

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Rothman et al.

Serial No.: 10/675,486

Filed: September 30, 2003

Assignee: Intel Corporation

For: "METHODS AND APPARATUS TO PROVIDE CONDITIONAL LEGACY SUPPORT"

Group Art Unit: 2127

Examiner: Unknown

I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date:

DATED: 1/15/2004

Mark C. Zimmerman
Registration No. 44,006
Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir or Madam:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

☒ No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.
20 N. Wacker Drive
Suite 4220
Chicago, Illinois 60606
(312) 580-1020

By: 
Mark C. Zimmerman
Registration No.: 44,006

DATED: 1/15/2004

Attorneys for Intel Corporation